# 3V, SUPER MINIMOLD UPC2771TB MEDIUM POWER SI MMIC AMPLIFIER

#### **FEATURES**

- · HIGH GAIN: 21 dB at 900 to 1500 MHz Typical
- HIGH OUTPUT POWER: PSAT = +12.5 dBm at 900 MHz +11 dBm at 1500 MHz
- LOW BIAS VOLTAGE: 3.0 V Typical, 2.7 V Minimum
- SUPER SMALL PACKAGE: SOT-363
- TAPE AND REEL PACKAGING OPTION AVAILABLE

#### DESCRIPTION

NEC's UPC2771TB is a Silicon Monolithic integrated circuit which is manufactured using the NESAT<sup>M</sup> III process. The NESAT III process produces transistors with f<sup>T</sup> approaching 20 GHz. The UPC2771TB is pin compatible and has comparable performance as the larger UPC2771T, so it is suitable for use as a replacement to help reduce system size. The IC is housed in a 6 pin super minimold or SOT-363 package. Operating on a 3 volt supply, this IC is ideally suited for hand-held, portable designs.

NEC's stringent quality assurance and test procedures ensure the highest reliability and performance.

#### GAIN vs. FREQUENCY AND TEMPERATURE



Frequency, f (GHz)

# **ELECTRICAL CHARACTERISTICS** (TA = 25°C, ZL = ZS = 50 Ω, Vcc = 3.0 V)

PART NUMBER PACKAGE OUTLINE					UPC2771TB S06		
SYMBOLS	MIN TYP		MAX				
Icc	Circuit Current (no signal)	mA		36	45		
Gs	Small Signal Gain,	f = 900 MHz f = 1500 MHz	dB dB	19 18	21 21	24 24	
f∪	Upper Limit Operating Frequency (	The gain at fu is 3 dB down from the gain at 100 MHz)	GHz	1.8	2.2		
P1dB	1 dB Compressed Output Power,	f = 900 MHz f = 1500 MHz	dBm dBm	+9 +7	+11.5 +9.5		
PSAT	Saturated Output Power,	f = 900 MHz f = 1500 MHz	dBm dBm		+12.5 +11		
NF	Noise Figure,	f = 900 MHz f = 1500 MHz	dB dB		6 6	7.5 7.5	
RLIN	Input Return Loss,	f = 900 MHz f = 1500 MHz	dB dB	10 10	14 14		
RLOUT	Output Return Loss,	f = 900 MHz f = 1500 MHz	dB dB	6.5 5.5	9.0 8.5		
ISOL	Isolation,	f = 900 MHz f = 1500 MHz	dB dB	25 25	30 30		
OIP3	SSB OutputThird Order Intercept Point	f = 900, 902 MHz, Pout = +4 dBm f = 1500, 1502 MHz, Pout = +4 dBm	dBm dBm		+13 +10		
Padji	Adjacent Channel Power 1,	f = 900 mHz, $\pi/4$ QPSK wave <sup>1</sup> , POUT = +7 dBm $\Delta f = \pm 50$ kHz $\Delta f = \pm 100$ kHz	dBc dBc		-61 -72		
Padj2	Adjacent Channel Power 2,	f = 1.5 GHz, $\pi/4$ QPSK wave <sup>1</sup> , POUT = +7 dBm $\Delta f = \pm 50 \text{ kHz}$ $\Delta f = \pm 100 \text{ kHz}$	dBc dBc		-59 -72		

Note:

1. $\pi$ /4 QPSK modulated wave input, data rate 42 kbps, Filter roll off  $\alpha$  = 0.5

#### ABSOLUTE MAXIMUM RATINGS<sup>1</sup> (TA = 25°C)

SYMBOLS	PARAMETERS	UNITS	RATINGS
Vcc	Supply Voltage	V	3.6
Icc	Total Supply Current	mA	77.7
Pin	Input Power	dBm	+13
Рт	Total Power Dissipation <sup>2</sup>	mW	200
Тор	Operating Temperature	°C	-40 to +85
Tstg	Storage Temperature	°C	-55 to +150

Notes:

1. Operation in excess of any one of these parameters may result in permanent damage.

2. Mounted on a 50 X 50 X 1.6 mm epoxy glass PWB (TA = 85°C).

#### RECOMMENDED OPERATING CONDITIONS

SYMBOLS	PARAMETERS	UNITS	MIN	TYP	MAX
Vcc	Supply Voltage	V	2.7	3	3.3
Тор	Operating Temperature	°C	-40	+25	+85

#### TYPICAL PERFORMANCE CURVES (TA = 25°C)



**CIRCUIT CURRENT vs. VOLTAGE** 

GAIN AND NOISE FIGURE vs. FREQUENCY AND VOLTAGE



#### **TEST CIRCUIT**





CIRCUIT CURRENT vs. TEMPERATURE

INSERTION POWER GAIN vs. FREQUENCY AND TEMPERATURE



#### TYPICAL PERFORMANCE CURVES (TA = 25°)



OUTPUT POWER vs. INPUT POWER AND VOLTAGE





0 (g) -10 0 -10 0 -20 -20 -30 -40 -50 0.1 0.3 Frequency, f (GHz)

**ISOLATION vs. FREQUENCY** 

OUTPUT POWER vs. INPUT POWER AND TEMPERATURE



OUTPUT POWER vs. INPUT POWER AND TEMPERATURE



Input Power, PIN (dBm)



SATURATED OUTPUT POWER vs.



THIRD ORDER INTERMODULATION DISTORTION vs.



**OUTPUT POWER vs.** INPUT POWER AND VOLTAGE



SATURATED OUTPUT POWER vs. FREQUENCY AND TEMPERATURE



THIRD ORDER INTERMODULATION DISTORTION vs. OUTPUT POWER OF EACH TONE AND VOLTAGE



## **TYPICAL SCATTERING PARAMETERS** (TA = 25°C)





#### Vcc = Vout = 3.0 V, Icc = 35 mA\_

FREQUENCY	s	511	Sa	1	S1	2	s	22	к	
GHz	MAG	ANG	MAG	ANG	MAG	ANG	MAG	ANG		
0.1	0.045	19.7	10.570	-4.7	0.028	0.8	0.327	-6.2	1.65	
0.2	0.057	37.0	10.638	-9.5	0.028	5.0	0.325	-11.5	1.63	
0.3	0.075	41.3	10.775	-14.1	0.029	8.6	0.323	-16.2	1.58	
0.4	0.090	43.3	11.004	-19.4	0.030	11.1	0.326	-20.9	1.49	
0.5	0.105	42.2	11.275	-24.4	0.030	14.9	0.331	-26.4	1.45	
0.6	0.118	40.2	11.586	-30.0	0.031	15.8	0.342	-32.0	1.37	
0.7	0.138	34.9	12.041	-35.9	0.031	19.8	0.350	-37.3	1.29	
0.8	0.163	32.5	12.367	-42.1	0.032	20.1	0.359	-42.8	1.20	
0.9	0.186	29.4	12.844	-48.8	0.032	23.2	0.361	-49.4	1.15	
1.0	0.202	26.3	13.300	-56.6	0.032	23.9	0.371	-56.1	1.11	
1.1	0.219	21.7	13.771	-64.6	0.033	24.9	0.389	-62.5	1.03	
1.2	0.233	15.4	14.082	-73.5	0.033	26.6	0.400	-69.3	0.99	
1.3	0.252	8.4	14.365	-83.2	0.036	28.8	0.405	-75.4	0.92	
1.4	0.267	-0.1	14.336	-92.6	0.036	30.0	0.402	-83.6	0.91	
1.5	0.285	-6.8	14.142	-102.4	0.036	32.0	0.406	-91.6	0.90	
1.6	0.293	-13.9	13.929	-112.0	0.037	31.6	0.413	-99.3	0.89	
1.7	0.304	-20.9	13.428	-121.6	0.039	32.5	0.414	-105.8	0.88	
1.8	0.290	-28.1	12.722	-131.0	0.038	34.7	0.401	-113.7	0.96	
1.9	0.285	-35.3	11.966	-139.6	0.038	36.1	0.387	-120.8	1.03	
2.0	0.273	-41.8	11.232	-147.5	0.038	37.4	0.378	-127.6	1.09	
2.1	0.267	-47.4	10.500	-154.8	0.039	39.1	0.366	-133.1	1.14	
2.2	0.254	-51.6	9.815	-161.7	0.040	41.4	0.356	-138.0	1.20	
2.3	0.237	-57.1	9.168	-168.0	0.041	43.7	0.342	-142.8	1.28	
2.4	0.221	-61.1	8.570	-173.7	0.041	48.3	0.325	-148.3	1.37	
2.5	0.212	-68.8	7.967	-179.7	0.042	48.3	0.322	-152.6	1.44	
2.6	0.208	-72.2	7.507	174.9	0.043	50.8	0.314	-156.7	1.49	
2.7	0.202	-74.1	7.004	170.0	0.045	53.7	0.309	-160.1	1.53	
2.8	0.190	-76.3	6.667	164.7	0.047	54.2	0.303	-164.0	1.56	
2.9	0.178	-76.7	6.336	160.7	0.051	57.7	0.292	-167.8	1.55	
3.0	0.154	-82.3	6.003	155.6	0.051	56.5	0.287	-172.8	1.62	
3.1	0.147	-88.0	5.772	151.3	0.054	59.3	0.279	-176.4	1.61	

#### OUTLINE DIMENSIONS (Units in mm)

#### UPC2771TB PACKAGE OUTLINE S06



### LEAD CONNECTIONS

4. OUTPUT

5. GND 6. Vcc



#### **PIN DESCRIPTION**

Pin No.	Pin Name (V)	Applied Voltage	Description	Internal Equivalent Circuit
1	Input	-	Signal input pin. An internal matching circuit, configured with resistors, enables 50 $\Omega$ connection over a wide bandwidth. A multi-feedback circuit is designed to cancel the deviations of hFE and resistance. This pin must be coupled to the signal source with a blocking capacitor.	
4	Output		Signal output pin. Connect an inductor between this pin and VCC to supply current to the internal output transistors.	
6	VCC	2.7 to 3.3	Power supply pin. This pin should be externally equipped with a bypass capacitor to minimize ground impedance.	
2	GND	0	Ground pins. These pins should be connected to system ground with minimum inductance. Ground pattern on the board should be formed as wide as possible. All the ground pins must be connected together with wide ground pattern to minimize impedance difference.	

#### **ORDERING INFORMATION**

PART NUMBER	QTY
UPC2771TB-E3-A	3K/Reel

Note: Embossed Tape, 8 mm wide. Pins 1, 2 and 3 face perforated side of tape.

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Restricted Substance per RoHS	Concentration Limit per RoHS (values are not yet fixed)	Concentration contained in CEL devices		
Lead (Pb)	< 1000 PPM	-A Not Detected	-AZ (*)	
Mercury	< 1000 PPM	Not De	etected	
Cadmium	< 100 PPM	Not Detected		
Hexavalent Chromium	< 1000 PPM	Not De	etected	
РВВ	< 1000 PPM	Not De	etected	
PBDE	< 1000 PPM	Not Detected		

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